

Arecont Vision - FPGA Engineer Opportunity

Location: Glendale, CA

Type: Full Time

Min. Experience: Experienced

Description

We are looking for Sr. FPGA Engineer to join our engineering team to develop image processing modules for our megapixel IP security cameras. If you are a top-notch engineer, with experience in FPGA development, we'd love to tell you more about this amazing opportunity.

Qualifications

- FPGA development experience, with DSP implementation, including RTL coding, synthesis and implementation;
- Solid background in digital image processing, compression, algorithms and analysis;
- Experience in PCB design;

Requirements

- PhD, MS or BS in Computer Science, Electrical Engineering, or a related technical discipline;
- Solid digital electronics development skills and ability to create production quality designs;
- Excellent written and verbal communication skills;
- Ability to work well in a collaborative team environment;

Our Company

Arecont Vision is exclusively focused on megapixel technology and, as the industry leader, we have contributed more to the advancement of megapixel imaging for professional security applications than any other company in the security market. As a result of our best-in-class product architecture, we typically introduce new imaging technology years ahead of competitors because we don't have to wait until a new off-the-shelf ASIC is available.

We are a fast growing 12-year-old company where key contributors are valued highly. In order to maintain our leading position in the marketplace and stay on the cutting edge of technology, the engineering and product development results are quickly propagated into products, and individual contributions are rewarded. We pay very competitive salaries to allow every key employee to fully share in our success.

For applying to the position, PLEASE visit the link on our website
(<https://www.arecontvision.com/career-opportunities.php>)

Alternatively, emails can be sent to HR at : hr@arecontvision.com and HR will relay all resumes to the appropriate departments.