

Spring 2012

Analog Microsystem Design

Instructor: Behzad Razavi
56-147D, Eng. IV
(310) 206-1633, razavi@ee.ucla.edu
Office Hours: Tue., Thur., 1:30-3:00 pm

Time: TR, 4:00-5:50 pm

Place: BL8500

Prerequisites: EE215A

Credit: 4 Units

Note: No use of laptops is allowed during lecture.

Grading:

Midterm	30%
Final	30%
Homeworks	20% (Late HW policy: 25%/day deduction) (Unstapled homeworks will not be graded.)
Final Project	20%

Course Textbook:

B. Razavi, *Principles of Data Conversion System Design*, IEEE Press, 1995

Reference Books:

R. Gregorian and G. Temes, *Analog MOS Integrated Circuits*, Wiley, 1986.

R. van de Plassche, *Analog-to-Digital and Digital-to-Analog Converters*, Kluwer, 1994.

Important Dates:

Thur. April 12

Tue. April 24

Tue. May 1

Tue. May 8

Thur. May 10

Fri. June 8

Mon., June 11, 6:30-9:30 pm

HW#1 Due

HW#2 Due

HW#3 Due

HW#4 Due

Midterm Exam

Final Project Due

Final Exam

Outline

- Introduction to Switched-Capacitor Circuits
- Sampling Circuits and Architectures
- Digital-to-Analog Conversion
- Analog-to-Digital Conversion
- Oversampled Converters
- Building Blocks
- Precision Techniques