

EE215A

Midterm Exam

Fall 2008

Name: *Solutions*.....

Time Limit: 1 hour and 50 minutes

Open Book, Open Notes

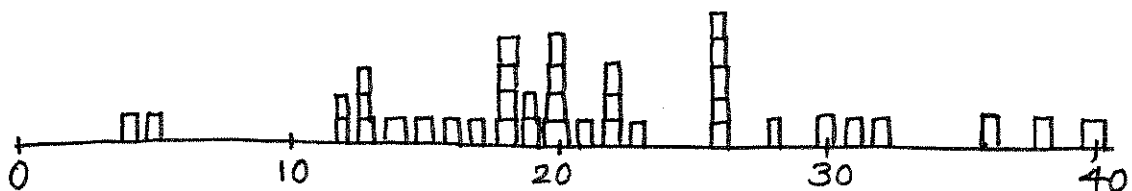
1. 10

2. 10

3. 10

4.  $\frac{10}{40}$

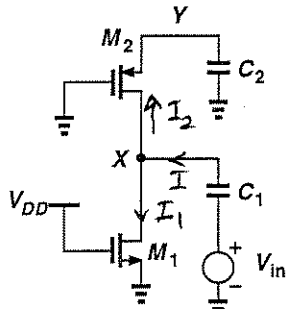
*Distribution*



*Avg. = 20/40*

*Mean = 20/40*

1. Consider the circuit shown below, where the initial voltage across  $C_1$  and  $C_2$  is zero and  $\lambda = \gamma = 0$ . At  $t = 0$ ,  $V_{in}$  jumps from 0 to 3 V. If  $C_1 = C_2$  and  $M_1$  is much weaker than  $M_2$ , sketch  $V_X$  and  $V_Y$  as a function of time. Explain your plots in detail. Assume  $V_{THN} = |V_{THP}| = 0.4$  V.



At  $t=0^+$ , both transistors are in sat.

$I = I_1 + I_2$  Since  $M_1$  is much weaker than  $M_2$   
 $\Rightarrow I_1 \ll I_2$

$\Rightarrow I \approx I_2 \Rightarrow -C_1 \frac{dV_X}{dt} = C_2 \frac{dV_Y}{dt}$   
 $C_1 = C_2 \Rightarrow -\Delta V_X = \Delta V_Y$

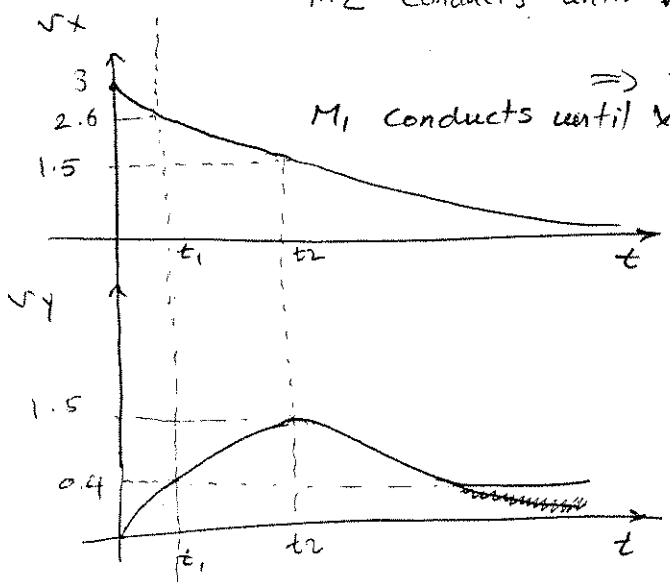
at  $t = t_1$   $\left\{ \begin{array}{l} V_X = 2.6 \\ -\Delta V_X = \Delta V_Y = 0.4 \end{array} \right. \rightarrow$  both  $M_1$  and  $M_2$  enter triode

at  $t = t_2$  :  $-\Delta V_X = \Delta V_Y = 1.5$  V  $\Rightarrow$   $M_2$  turns off

$\Rightarrow I = I_1 \Rightarrow C_1$  discharges through  $M_1$  and reduces voltage of  $V_X \Rightarrow M_2$  conducts again and reduces  $V_Y$

$M_2$  conducts until  $V_Y$  reaches the threshold voltage

$\Rightarrow V_Y(\infty) = 0.4$   
 $M_1$  conducts until  $V_X$  reaches zero.  $\Rightarrow V_X(\infty) = 0$ .



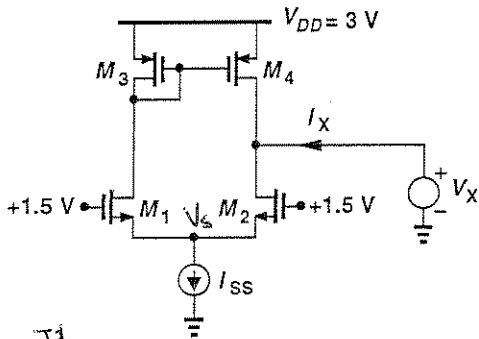
$M_1$  Sat  $\leftarrow \rightarrow$   $M_1$  triode  
 $M_2$  Sat  $\leftarrow \rightarrow$   $M_2$  triode

2. Assuming  $\lambda = \gamma = 0$ ,  $V_{THN} = |V_{THP}| = 0.4$  V,  $M_1$  and  $M_2$  are identical, and  $M_3$  and  $M_4$  are identical,

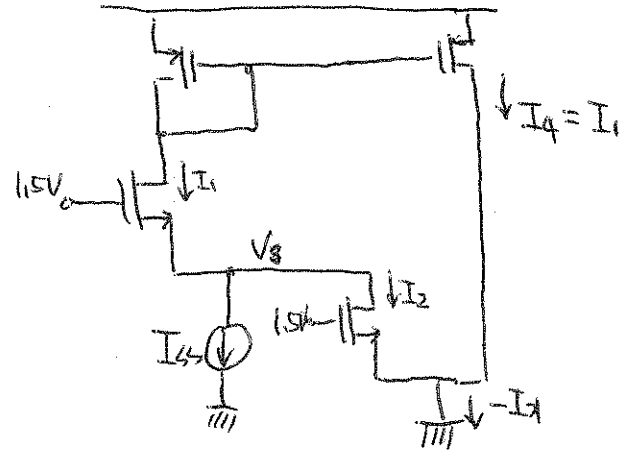
(a) Sketch  $I_X$  as a function of  $V_X$  as  $V_X$  goes from 0 to  $V_{DD}$ . Explain the details of your plot.

(b) For  $V_X = 0$ , construct *one* equation that expresses the value of  $I_X$  in terms of device and circuit parameters.

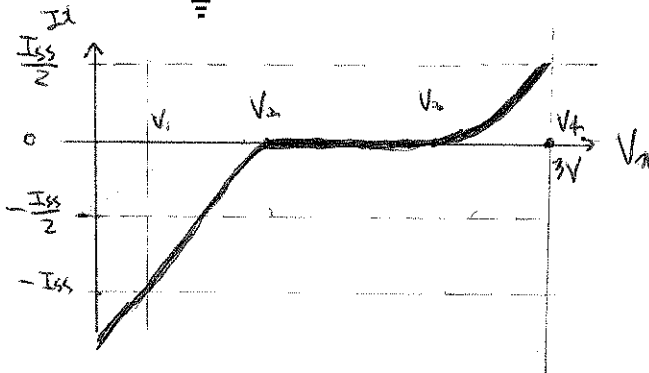
You need not solve the equation.



(b) Eq ckt  $V_X = 0$



(a)



MOS in SAT if not stated otherwise,

i)  $0 \leq V_X < V_1$   $M_2$  triode,  $V_2 = 1.1$ ,  $S \leftrightarrow D @ V_1$

ii)  $V_X = V_1$   $I_{D2} = 0$ ,  $V_1 = 1.1 - \sqrt{\frac{2 I_{SS}}{\mu_n C_{ox} (\frac{W}{L})_{1,2}}}$

iii)  $V_2 \leq V_X \leq V_3$  All sat  $\therefore I_X = 0$  ( $\therefore I_i = \frac{I_{SS}}{2}$   $i = 1, 2, 3, 4$ )

iv)  $V_3 < V_X < V_4$   $M_4$  triode  $V_3 = 3 - 0.4 - \sqrt{\frac{I_{SS}}{\mu_p C_{ox} (\frac{W}{L})_{3,4}}}$

v)  $V_X = V_{DD}$   $I_X = 0$   $\therefore I_1 = I_2 = \frac{I_{SS}}{2}$

6 pnts

(b) Let  $\alpha_n \equiv \mu_n C_{ox} (\frac{W}{L})_{1,2}$ ,

Using KCL

$$\begin{cases} -I_X = I_1 + I_2 & \text{--- (1)} \\ I_{SS} = I_1 - I_2 & \text{--- (2)} \end{cases} \Leftrightarrow \begin{cases} I_X = -(I_{SS} - 2I_2) \\ = -(2I_1 - I_{SS}) & \text{--- (3)} \end{cases}$$

4 pnts

$$I_1 = \frac{1}{2} \alpha_n (V_{GS} - V_{TH})^2 = \frac{1}{2} (V_G - V_S - V_{TH})^2$$

$$= \frac{1}{2} \alpha_n (1.1 - V_S)^2 \quad \text{--- (4)}$$

$$I_2 = \frac{1}{2} \alpha_n (2(V_{GS} - V_{TH})V_{DS} - V_{DS}^2)$$

$$= \frac{1}{2} \alpha_n (2 \cdot 2 V_S - V_S^2) \quad \text{--- (5)}$$

plugging ④ & ⑤ into ②,

$$I_{ss} = \frac{1}{2} \alpha_n (1.1 - V_s)^2 - \frac{1}{2} \alpha_n (2.2 V_s - V_s^2)$$

$$\frac{2I_{ss}}{\alpha_n} + 1.1^2 = 2(V_s - 1.1)^2$$

$$\therefore V_s = 1.1 - \sqrt{\frac{1}{2} \left( \frac{2I_{ss}}{\alpha_n} + 1.1^2 \right)} \quad \text{--- ⑥}$$

plug ⑥ into ④

$$\begin{aligned} I_1 &= \frac{1}{2} \alpha_n \left( 1.1 - 1.1 + \sqrt{\frac{1}{2} \left( \frac{2I_{ss}}{\alpha_n} + 1.1^2 \right)} \right)^2 \\ &= \frac{1}{4} \alpha_n \left( \frac{2I_{ss}}{\alpha_n} + 1.21 \right) \quad \text{--- ⑦} \end{aligned}$$

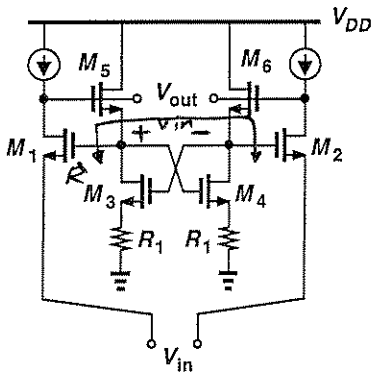
now using ④ & ③

$$I_2 = -(2I_1 - I_{ss})$$

$$\therefore I_2 = -\frac{1.21}{2} \alpha_n$$

(note)  $I_2|_{V_D=0} < -I_{ss}$  "

3. Assuming perfect symmetry and  $\lambda = \gamma = 0$ , determine the differential voltage gain and input impedance of the circuit shown below.



$$V_{out} = V_{GS5} + V_{in} - V_{GS6}$$

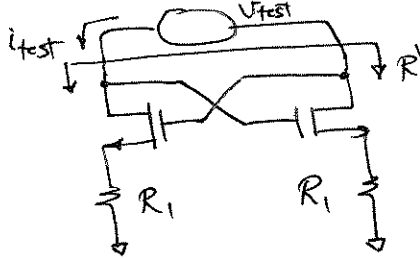
$$V_{GS5} = \frac{i_s}{g_{m5,6}}$$

$$V_{GS6} = \frac{i_c}{g_{m5,6}}$$

$$\text{but } i_c = -i_s \rightarrow V_{GS5} - V_{GS6} = \frac{2 \times i_s}{g_{m5,6}}$$

But

$$i_s = \frac{V_{in}}{R'}$$



$$R' = \frac{V_{test}}{i_{test}}$$

$$V_x = i_{test} \cdot R_1$$

$$i_{test} = g_{m3,4} \left( -\frac{V_{test}}{2} - V_x \right)$$

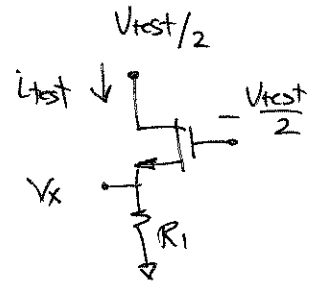
$$\therefore i_{test} = \frac{-g_{m3,4}}{2} V_{test} - g_{m3,4} R_1 i_{test}$$

$$\rightarrow \frac{V_{test}}{i_{test}} = - \frac{2(1 + g_{m3,4} R_1)}{g_{m3,4}} = R'$$

$$\Rightarrow V_{out} = V_{in} + 2 \times \frac{V_{in}}{R'} \times \frac{2}{g_{m5,6}} =$$

$$V_{out} = V_{in} \left( 1 + \frac{-g_{m3,4} / g_{m5,6}}{1 + g_{m3,4} R_1} \right)$$

half circuit:



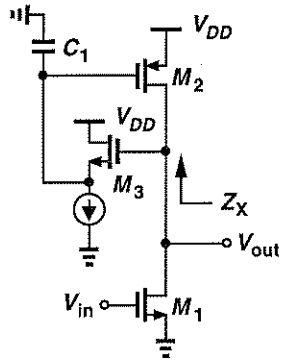
$$Z_{in} = \infty$$

(input current is always constant)

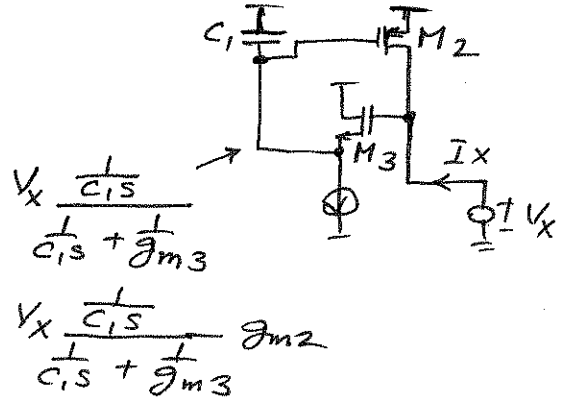
4. Neglecting all other capacitances in the circuit shown below and assuming  $\lambda = \gamma = 0$ ,

(a) Determine the transfer function  $V_{out}/V_{in}(s)$  and plot the magnitude as a function of frequency.

(b) Find an equivalent circuit consisting of only resistors, capacitors, and inductors for the load impedance  $Z_X$ .

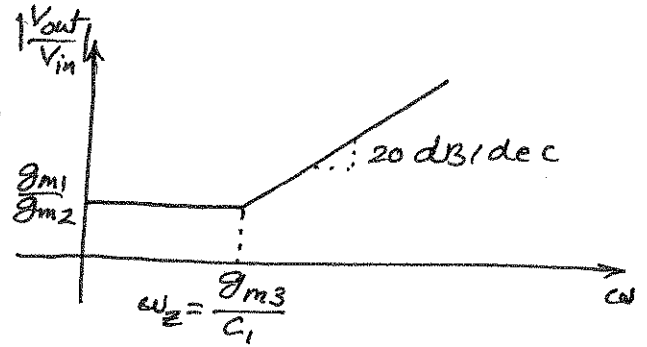


(a)



$$\Rightarrow Z_X = \frac{1}{g_{m2}} \left( 1 + \frac{C_1 s}{g_{m3}} \right)$$

$$\Rightarrow \frac{V_{out}}{V_{in}}(s) = \frac{g_{m1}}{g_{m2}} \left( 1 + \frac{C_1 s}{g_{m3}} \right)$$



(b)  $Z_X = \frac{1}{g_{m2}} + \frac{C_1}{g_{m2} g_{m3}} s$

