Undergraduate Research in the NanoCAD Laboratory

Summary of project(s):

Volunteer position is available for an undergraduate in the NanoCAD Laboratory, led by Prof. Puneet Gupta. Multiple projects may be available primarily in the areas of computer architecture, digital design and circuit layout algorithms. You will be working with a graduate student, hopefully resulting in a publication before you graduate. Students with desire to continue to grad school are welcome and preferred.

Website: http://nanocad.ee.ucla.edu

Required/recommended courses:

    Required: EEM16, CS31, CS32

    Useful but not required: EEM116C, CS180, EEM116L, CS152B, EE115C, CS33

Required/recommended skills or experience:

    Required: Interest and skills in programming (C++ for software and Verilog/VHDL for hardware). A minimum of 6 month commitment is required.

    Useful but not required: Experience with FPGAs, board design

Other Special requirements:

Date Position Available: Ongoing.

How to apply:

Email your application materials to Prof. Gupta at puneet@ee.ucla.edu

Curriculum vitae or resume, transcript (informal is OK)

Contact: **puneet@ee.ucla.edu**

Listing expires on: Never