



Title: Wireless PHY Design Verification Engineer (New College Grad)

Description:

You will be part of an exciting silicon design group that is responsible for designing state-of-the-art ASICs. We have an extraordinary opportunity for someone who wants to build a career in pre-silicon design verification. You will be part of Apple's Silicon Engineering group that turns imaginative and revolutionary ideas into reality. You will help design products that bring to our customers experiences they've never before envisioned.

In this role you will be responsible for and contributing to multiple aspects of the development of wireless subsystems' design verification efforts of Wireless PHY design, integration of Baseband PHY with Radio model and upper layer SOC. With a deep understanding of communication systems and protocols, you will interact with DV methodologists, designers and communication systems engineers to develop top down simulation platforms, reusable test benches and verification environments deploying the latest methodology with metric driven verification. You will be able to gain breadth and depth of knowledge in various wireless technology related IEEE standards, SOC infrastructure, state-of-art UVM based portable DV methodology, modeling aspects of complex systems and overall chip design, verification, simulation acceleration, prototyping platforms and development process.

Required Qualifications:

- Coursework in Digital Signal Processing (e.g. digital filter, modulation, demodulation, frequency estimation, channel estimation, amplitude gain control etc.)
- Good understanding of C/C++/Python and object-oriented programming paradigm
- Knowledge of logic design
- Some Knowledge of mixed signal simulation (ADC, DAC, Power Amplifier, LNA etc.) using RF behavior model and digital PHY's RTL is a plus but not required
- Some knowledge on design verification concepts and IEEE standards (e.g. 802.11) is a plus

Education:

MS (or PHD) in Electrical Engineering with concentration in Digital Signal Processing.

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