High-Speed Analog Designer Positions available @ BROADCOM Wireline Transceiver Design Group

Recent College Graduates & Experienced Candidates are all encouraged to apply

Immigration support is available

Please email your resume to: namik.kocaman@broadcom.com

Job Description
In this line of work, the prospective employee will specialize in high-speed analog circuit design for wireline transceivers

Determines design approaches and parameters. Analyzes equipment to establish operating data; conducts experimental tests and evaluates results. Selects components and equipment based on analysis of specifications and reliability. May also review vendor capability to support product development. Layout/design work is done in ASIC product environment. Works on problems of moderate scope where analysis of situations or data requires a review of a variety of factors. Exercises judgment within defined procedures and practices to determine appropriate action. Builds productive internal/external working relationships.

The prior industry or academic experience required (MSc or PhD) in one or more of the following areas:

- High-Speed Serializer / Deserializer Design (PAM8 / PAM4 / NRZ)
- High-Speed (>10Gbps) ADC and DAC Design
- High-Speed (>28GHz) and Low Jitter (<100fs-rms) PLL Design
- High Linearity (1-3% THD) Low-Noise TIA and Laser Driver Design
- Equalization Techniques for Backplanes and Optical Links
- High-Speed Low Latency Clock & Data Recovery Circuits
- High-speed RF techniques, LNA, Power Amplifier Circuits

The following skills are highly desirable:

- Circuit & Layout expertise in advanced process nodes (5nm , 7nm CMOS FinFet)
- Strong understanding and user experience in EM tools such as HFSS, EMX
- Strong DSP knowledge, and capability to analyze systems using Matlab and Verilog AMS

Please apply only if you have a long-term career goal in Analog Design and possess the necessary background. You should have graduate studies and/or research projects and/or relevant industry experience in the area of analog circuits.

There will be a rigorous technical interview process. Please prepare for analog design fundamentals along with your resume experience.