For students who are interested, please apply on the UCLA career center portal, https://app.joinhandshake.com .

- At Broadcom's Wireless Communications and Connectivity division, we are actively seeking talented **RF Design Engineers** who want to join a dynamic and experienced team to take their technical knowledge to the next level. This position open to RF, RFIC, RF Systems, RF Applications engineers. Responsibilities include bring-up, optimization, verification, and productization of highly integrated transceivers designed for WLAN applications. You will be central to optimize transceiver parameters such as phase noise, IQ imbalance, power, gain, spurs, noise figure, AGC, filter response in order to optimize system performance such as EVM, PER, ACPR. Other skills needed include developing python/tcl programs to efficiently accomplish optimization, develop test plan/strategy to screen at production, and perform failure analysis to resolve system malfunctions reported by customers.

- Ideal RF Design Engineer candidates will have:
    - Minimum requirement MSEE
    - RF Systems course/design with emphasis on digital communications
    - RF CMOS course/design background
    - Passion for his/her work, a self-starter, and works well in a team environment

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- At Broadcom's Wireless Communications and Connectivity division, we are actively seeking talented **RFIC Design Engineers** who want to join a dynamic and experienced team to take their technical knowledge to the next level. You will participate in the design of WiFi and Bluetooth radios including next generation deep sub-micron CMOS. Specifically, you will own the design and development of RFIC transceiver sub-blocks: low noise amplifiers, power amplifiers, VGAs, mixers, RF PLLs & synthesizers, analog filters, adcs, and dacs utilizing state-of-the-art tools (cadence, matlab, ADS) You may also help with transistor modeling for RF design, RF board/module design, and lab characterization.

- Ideal RFIC Design Engineer candidates will have:
    - Minimum requirement MSEE
    - RF CMOS course/design background:
    - RF blocks used in transmitters and receivers
        - Front-end (LNA, mixers, PA drivers, PAs)
        - PLLs & Frequency Synthesizers
        - Transceiver baseband blocks (filters, VGA, ADCs & DACs)
    - Passion for his/her work, a self-starter, and works well in a team environment