Digital System Engineer

SiLC is looking to hire a digital system engineer to be a part of a dynamic and experienced team focused on building a revolutionary 4D sensing solution. This is a full-time position and not an internship position.

Responsibilities:

- Test and verification of digital sub-modules and systems based on Xilinx SoC devices
- Lidar data collection using the complete digital system, analog electronics, integrated photonics, and bulk optics
- Analysis and post-processing of the data
- Perform hands-on verification and debug of hardware/firmware
- Documenting the results and communicating it to technical staff

Qualifications:

- M.Sc. with 3+ years of experience
- Familiarity with Xilinx FPGAs, Vivado, Vitis, and SDK
- Experience with Verilog/SystemVerilog
- Familiar with Linux and embedded programming in C/C++
- Hands-on experience with lab equipment (oscilloscope, spectrum analyzer, VNA, signal generator) for hardware bring-up and characterization

Preferred Qualification:

- Experience with Xilinx SoC FPGAs, knowledge of their block design editor, IP catalog, and ILA
- Experience with signal processing for communication, radar, or lidar systems

About SiLC

We are a 4D vision sensor company located in Monrovia, CA. Our focus is on creating 3D point clouds based on FMCW lidar concept and coherent optical detection. Our lidar sensor is capable of measuring more than just the distance of an object, unlocking multitude of new dimensions that can enhance performance of modern machine vision algorithms. We just closed our series A and are expanding our team to scale up our activities. Please see the link below for the most recent updates.

NEWS | SILC May 2021

Contact at SiLC
Behnam Behroozpour
behnam@silc.com

Hardware Test Engineer

SiLC is looking to hire a hardware test engineer to be a part of a dynamic and experienced team focused on building a revolutionary 4D sensing solution. This position could be filled as a full-time position for a recent graduate or an at least six months long internship that turns into a full-time position for a current MS student.

Responsibilities:

- Lidar data collection using the complete digital system, analog electronics, integrated photonics, and bulk optics
- Analysis and post-processing of the data
- Perform hands-on verification and debug of hardware/firmware
- Documenting the results and communicating it to technical staff

Qualifications:

- B.Sc. with 4+ years of experience or MS
- Experience with Linux and embedded programming in C/C++
- Hands-on experience with lab equipment (oscilloscope, spectrum analyzer, VNA, signal generator) for hardware bring-up and characterization

Preferred Qualification:

- Familiar with Verilog/SystemVerilog
- Experience with Xilinx FPGAs, Vivado, Vitis, and SDK
- Experience with signal processing for communication, radar, or lidar systems

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Contact at SiLC
Behnam Behroozpour
behnam@silc.com

Senior Digital System Engineer

SiLC is looking to hire a senior digital system engineer to be a part of a dynamic and experienced team focused on building a revolutionary 4D sensing solution. This is a full-time position and not an internship position.

Responsibilities:

- Integration of digital sub-modules into a full system consisting of Xilinx SoC devices, analog electronics, integrated photonics and bulk optics
- Perform hands-on verification and debug of hardware/firmware
- Documenting the results and communicating it to technical staff

Qualifications:

- Ph.D. or M.Sc. with 3+ years of experience
- Experience with Xilinx SoC FPGAs, Xilinx Vivado; SDK, knowledge of their block design editor, IP catalog, and ILA
- Creating projects, first stage boot loaders (FSBL), and boot images
- Proficient in Verilog/SystemVerilog
- Experience with Linux and embedded programming in C/C++
- Experience in programming/scripting using Perl, shell scripting
- Hands-on experience with lab equipment (oscilloscope, spectrum analyzer, VNA, signal generator) for hardware bring-up and characterization

Preferred Qualification:

- Experience with DDR4, JESD 204B/C, parallel DDR LVDS, and Ethernet MAC IPs
- Experience with signal processing for communication, radar, or lidar systems

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