



Mx Imaging, Inc.
2894 Columbia Street
Torrance, CA 90503
Tel: (310)381-3800
FAX:(310)381-3804

Job Description

FPGA and board level Hardware design engineer responsible for definition and design of company's image acquisition and processing HW. Responsibilities include: FPGA design using VHDL, schematic design and initial test and debug of new boards.

Qualifications:

- Hands on experience with HW Design and Test
- Hand on experience with VHDL, Xilinx FPGAs and FPGA design tools
- Familiarity with Video/Graphics principles
- Familiarity with Image processing concepts
- Ability to create detailed design documentation.
- Ability to work on multiple projects in parallel.
- Proficient in Microsoft Word.
- Good communication skills and problem solving abilities
- BS/MS in Computer Engineering or Computer Science.

-Good communication skills and problem solving abilities

-BS/MS in EE

Company Profile:

Located in Torrance CA, Mx Imaging is engaged in the business of Design and Manufacture of state of the art Image Acquisition, Processing, Storage and Review Systems or Sub Systems used in the Medical Imaging field.

Salary and Benefits:

Salary: Industry Competitive

Benefits: Industry Competitive