



Internship Opening - Qualcomm Analog/RF IC Design Engineer (Irvine)

Job Function: Qualcomm is a leading provider of wireless and wired technologies for the mobile, networking, computing and consumer electronics markets. We are focused on inventing technologies that connect and empower people in ways that are elegant and accessible to all. Our team is responsible for the complete design lifecycle, from system-level concept to tape out and post-silicon support, of advanced SOC designs in advanced CMOS processes. We work on highly integrated mixed-signal circuits, Giga Hertz radio frequency transceivers and architectures.

Responsibilities: Apply your analog/RF integrated circuit design expertise to the development of highly integrated CMOS transceivers for wireless communication systems. Circuits of interest may include power amplifiers, line drivers, low noise amplifiers, transmit and receive mixers, frequency synthesizer, baseband amplifiers and filters, voltage regulators and bandgap circuits, phase-locked loops (PLLs) and delayed-lock loops (DLLs), data converters (DACs/ADCs), and switched-capacitor circuits. The candidate intern for 3 months in our Irvine office in California.

Skills/Experience:

MS or PhD student in EE (with focus on RFIC design)

Knowledge of highly integrated mixed-signal circuits, RF system and architecture.

Familiarity with Cadence, Spice, and Matlab simulations is required.

The candidate must be comfortable with working in a team environment.

Contact Information: Send your resumes to shervin@qti.qualcomm.com