Job Title:
Photo Lithography Process Engineer III

About Tower Semiconductor

Recruiting world-class talent for a world-class leader!

Join Tower Semiconductor!

Looking for a career path in the high tech manufacturing industry? Become part of a team focused on delivering the most exciting semiconductor technology to the world! If you enjoy working with others in a fast pace environment and are looking for an opportunity to grow your career in the high tech industry then Tower Semiconductor is the place to be!

About Tower Semiconductor

Tower Semiconductor is a global specialty foundry leader! We specialize in manufacturing analog integrated circuits for more than 300 customers worldwide in growing markets such as automotive, medical, industrial, consumer and aerospace and defense, among others.

Job Description

Responsibilities include all activities related to high volume 200mm wafer fabrication process sustaining, new process development, integration, and process improvement of Photo Lithography processes and related equipments.

Some experiences in ASML and Canon steppers and scanners for lithography, DNS and TEL track systems for resist and polyimide applications, AMAT SEM for inline CD measurement, defect inspection, Overlay Metrology tools such as IVS or KLA, and thin film metrology measurement tools will be helpful for the position. Good experience in stepper or scanner technology and Lithography is strongly preferred for this position.
Good knowledge in use of statistical methods and experimental design techniques include frequent use of JMP and Design Expert statistical analysis software, in addition to PROMIS as a statistical process control tool.

This Job Is For You If

- You can ensure world-class manufacturing quality and customer satisfaction through continuous improvement and strict adherence to six sigma process control practices.
- You can optimize processes and tools to cost effectively manufacture semiconductor integrated devices.
- You are able to coordinate and deliver key engineering solutions to new process development, integration and improvement of existing manufacturing systems related to Lithography using industry leading statistical methods and experimental design techniques.

Job Requirements

Minimum of 3 years of semi-conductor fabrication experience or education is required. Use of multi-disciplinary approaches to problem solving (DOE, etc.) is highly recommended.

Education/Licenses/Certifications

BS degree in engineering or pure science (chemistry, material science or physics) required. If lacks experience, MS or higher degree in science is required.

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Talent Acquisition Leader

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